

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Brahmadathan et al.

Confirmation No.: 7369

Serial No.: 10/820,178

Group Art Unit: 2133

Filed: April 7, 2004

Examiner: Joseph D. Torres

Title: METHOD, SYSTEM AND PROGRAM PRODUCT FOR AUTONOMOUS ERROR  
RECOVERY FOR MEMORY DEVICES

To: Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Response to Office Action**

Dear Sir:

This paper is filed responsive to the Office Action mailed March 12, 2007, in connection with the above-designated U.S. patent application. Allowance of the application is respectfully requested in view of the Amendments and Remarks below.

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Claims** begin on page 3 of this paper.

**Remarks** begin on page 10 of this paper.